WHAT IS CLAIMED IS:

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1. An active matrix type electroluminescence display device comprising:

a plurality of display pixels arranged in rows and columns in a matrix form;

gate signal lines, each of which is connected to and shared by a plurality of display pixels provided on each row; and

gate drive circuits for sequentially supplying select signals to said gate signal lines; wherein

each of said display pixels includes an electroluminescence element, a first thin film transistor in which a display signal is applied to the drain and which is switched on and off in response to said select signal, and a second thin film transistor for driving said electroluminescence element based on said display signal; and

said gate drive circuits are placed so that said select signals are supplied from both ends of said gate signal lines to said gate signal lines.

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2. An active matrix type electroluminescence display device according to claim 1, wherein

said gate drive circuits include a first and second gate drive circuits arranged in a symmetric pattern to the right and left of a display region constructed from said plurality of display pixels.

3. An active matrix type electroluminescence display device

according to claim 2, wherein

each of said first and second gate drive circuits includes a plurality of shift registers for sequentially shifting a reference clock with a pulse width of one horizontal period.

4. An active matrix type electroluminescence display device according to claim 3, wherein

each of said first and second gate drive circuits includes buffer amplifiers for driving said gate signal lines based on the output of said shift registers.

5. An active matrix type electroluminescence display device according to claim 4, wherein

the number of said shift registers and of the buffer amplifiers included in each of said first and second gate drive circuits corresponds to the number of rows of said plurality of display pixels.